TITLE: A DEVICE AND METHOD TO DETECT AND CORRECT FOR CLOCK DUTY CYCLE SKEW IN A PROCESSOR
INVENTORS NAME: Binglong Zhang
DOCKET NO.: 884.993US2


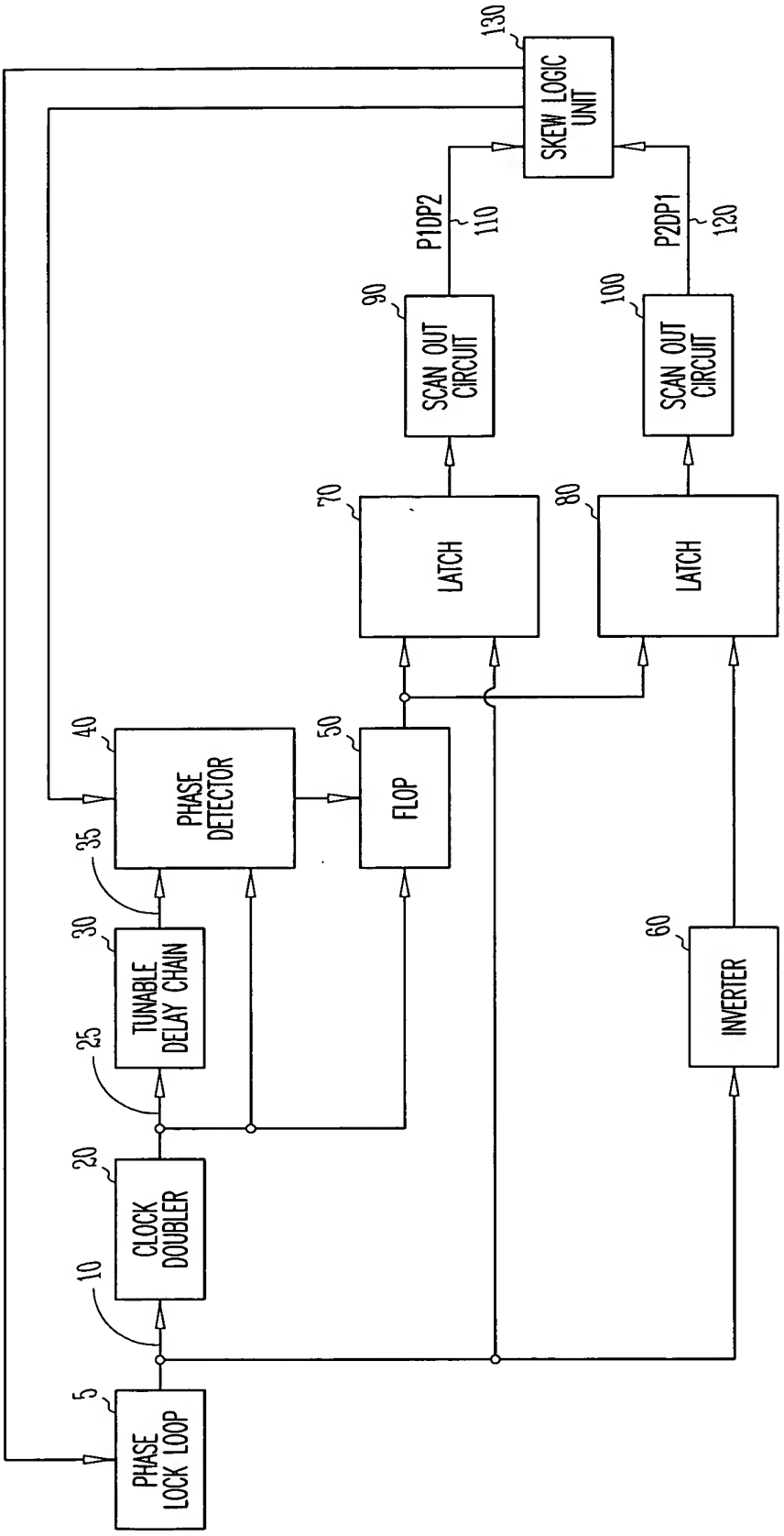


Fig. 1

FIG. 2

| FIG. 2A |
| --- |
| FIG. 2B |

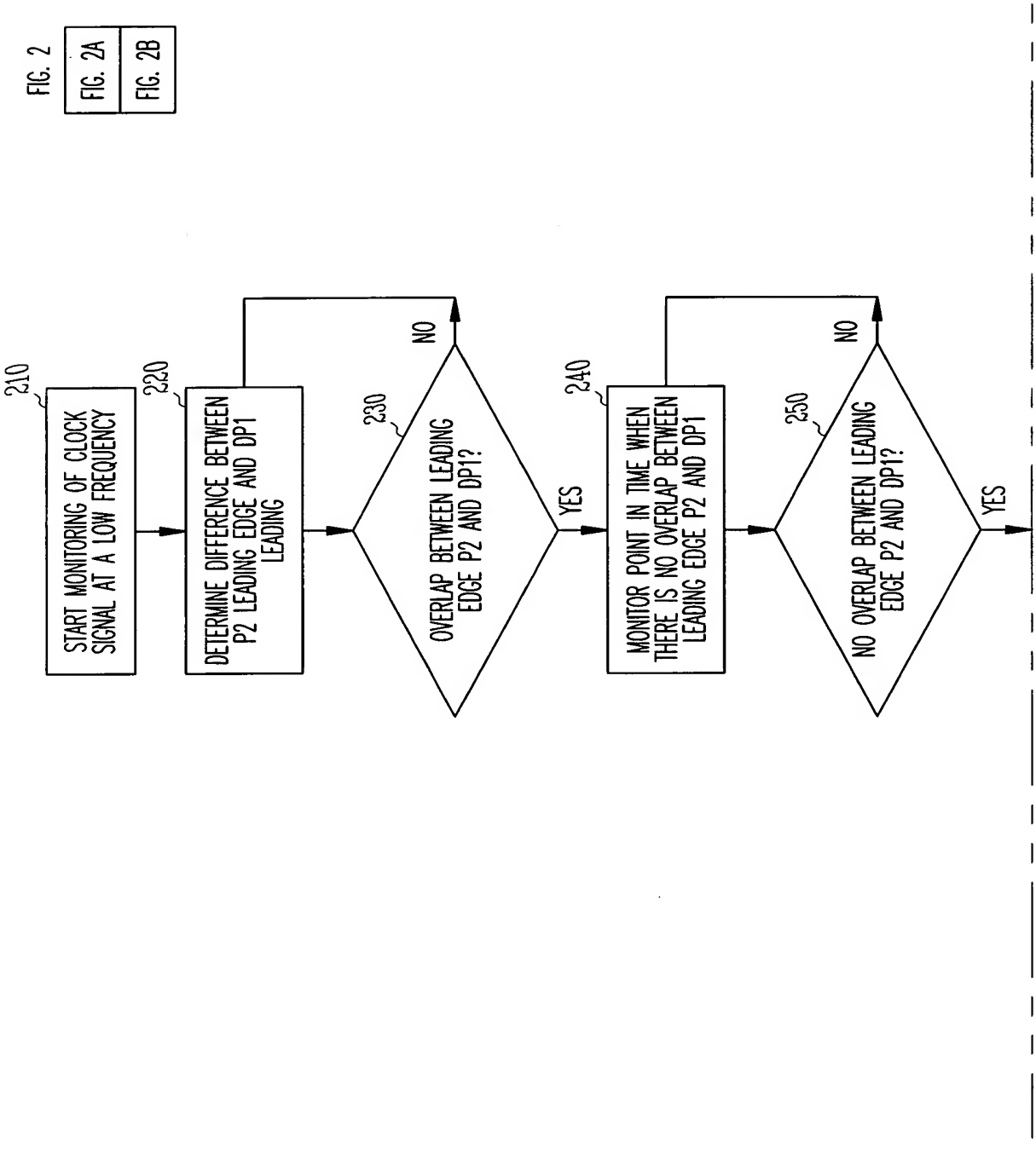


Fig. 2A

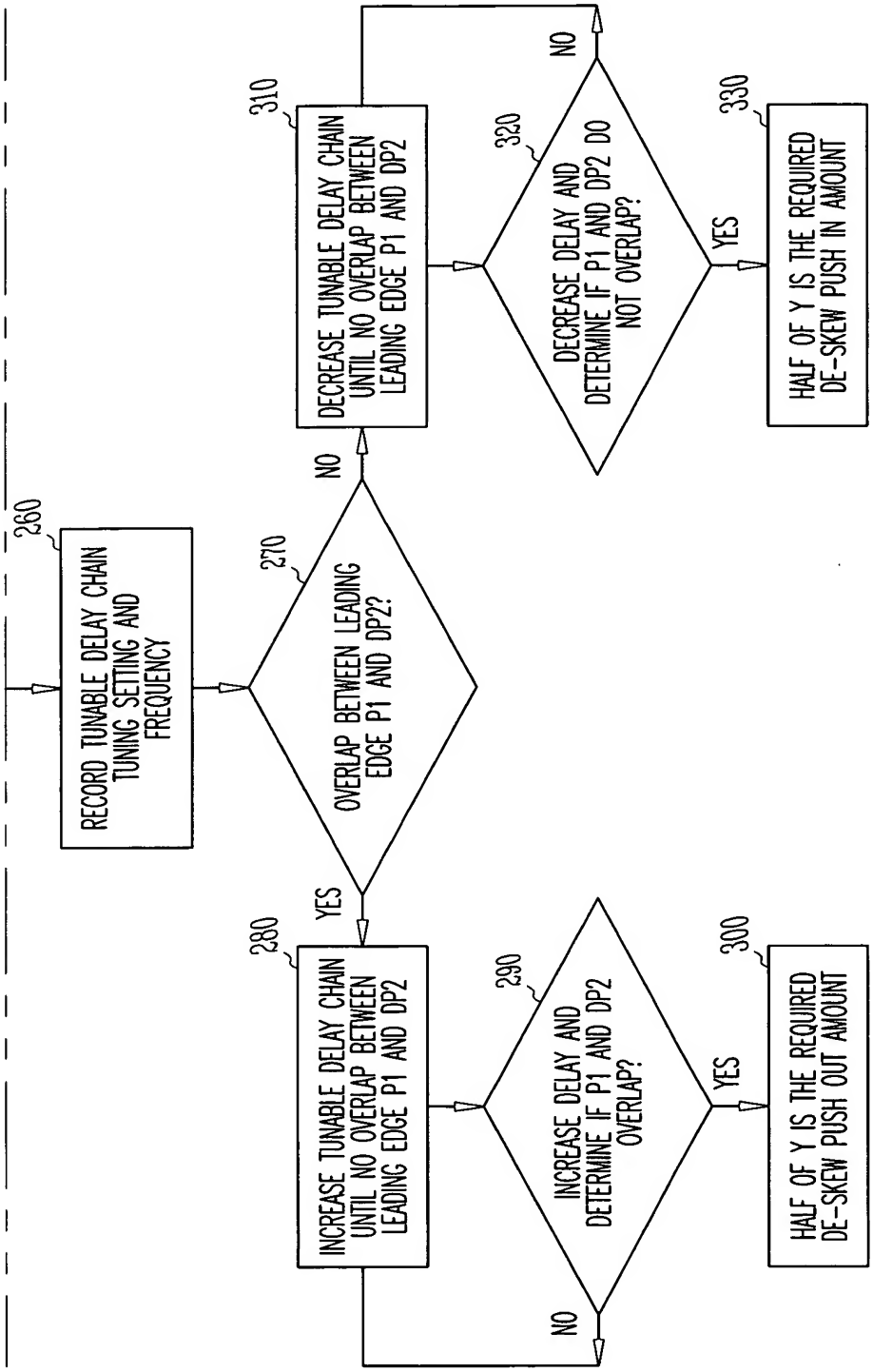
260
RECORD TUNABLE DELAY CHAIN TUNING SETTING AND FREQUENCY
270
OVERLAP BETWEEN LEADING EDGE P1 AND DP2?
NO
YES
310
DECREASE TUNABLE DELAY CHAIN UNTIL NO OVERLAP BETWEEN LEADING EDGE P1 AND DP2
320
DECREASE DELAY AND DETERMINE IF P1 AND DP2 DO NOT OVERLAP?
NO
YES
330
HALF OF Y IS THE REQUIRED DE-SKEW PUSH IN AMOUNT
280
INCREASE TUNABLE DELAY CHAIN UNTIL NO OVERLAP BETWEEN LEADING EDGE P1 AND DP2
290
INCREASE DELAY AND DETERMINE IF P1 AND DP2 OVERLAP?
NO
YES
300
HALF OF Y IS THE REQUIRED DE-SKEW PUSH OUT AMOUNT

Fig. 2B

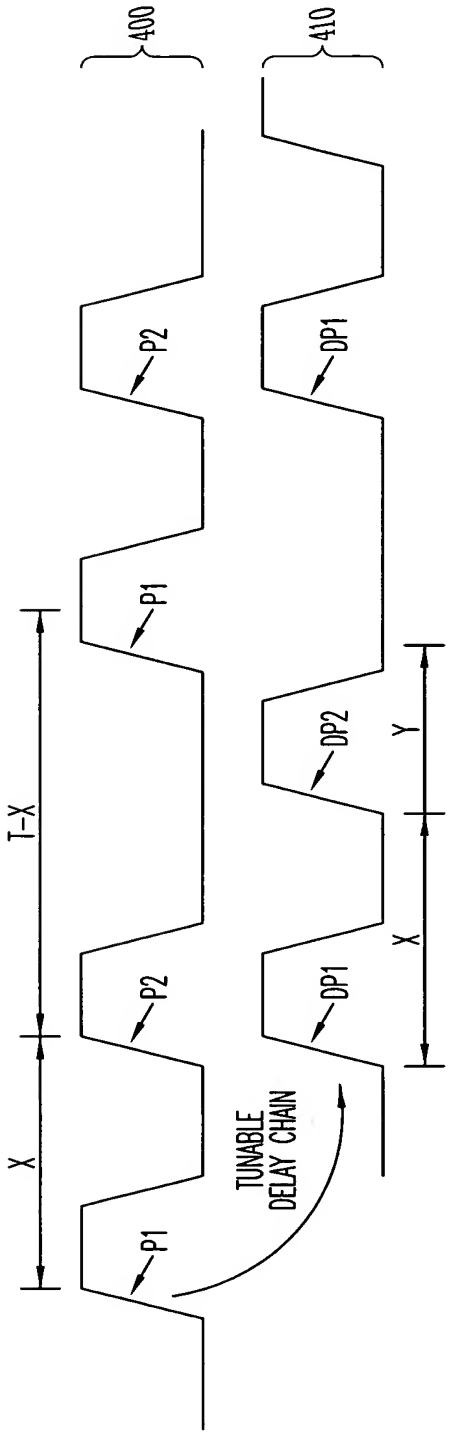


Fig. 3

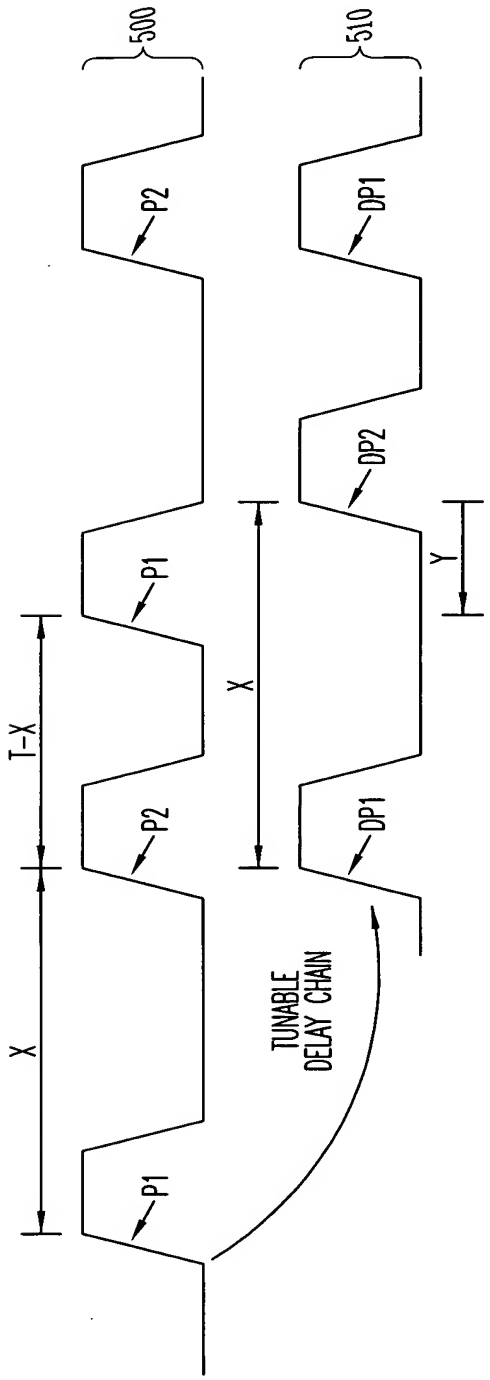


Fig. 4